



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Honeywell Case No. H0002270 DIV

(MBHB Ref. No. 06-308-E)

In the Application of: APR 12 2007

Mohammed A. Fathimulla and
Thomas Keyser

Serial No.: 10/764,938

Filing Date: January 26, 2004

For: Silicon-On-Insulator for RF Integrated
Circuit

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) Examiner: Long Pham
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) Group Art Unit: 2814
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TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In regard to the above-identified patent application:

1. We are transmitting herewith the attached:

- a) Transmittal Letter in Duplicate;
- b) PTO Form 1449; and
- c) Two (2) Cited References.

2. With respect to additional fees:

- ☐ A. No additional fee is required.
- ☒ B. Attached is a check in the amount of \$180.00.
- ☐ C. Charge the total fee due or credit any overpayment to our Deposit Account No. 13-2490. A duplicate copy of this sheet is enclosed.

3. CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this Transmittal Letter and papers, as described in paragraph 1 hereinabove, are being deposited with the United States Postal Service, with sufficient postage as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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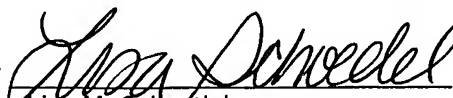
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Respectfully Submitted,
McDonnell Boehnen Hulbert & Berghoff LLP

Date: April 9, 2007

By


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Form PTO-1449 U.S. Department of Commerce Patent and Trademark Office INFORMATION DISCLOSURE STATEMENT BY APPLICANT 	Honeywell Ref. No. H0002270 DIV MBHB Case No. 06-308-E	Serial No. 10/764,938
	Applicant: Fathimulla et al.	
	Filing Date: January 26, 2004	Group: 2814

U.S. PATENT DOCUMENTS

Examiner Initial	No.	Document Number	Date	Name	Class	Subclass	Filing Date

FOREIGN PATENT DOCUMENTS

Examiner Initial	No.	Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No

OTHER DOCUMENTS - Including Author, Title, Date, Pertinent Pages, Etc.

Examiner Initial	No.	
	1.	A.B Glaser et al., "Integrated Circuit Engineering," p. 265-267, 289 Addison-Wesley, 1977
	2.	U.S. Davidsohn and F. Lee., "Dielectric-Isolated Integrated-Circuit Substrate Processes." Proc. IEEE 57: 1532-1537 (September 1969).

Examiner	Date Considered
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with any communication.